UNITED STATES PATENT AND TRADEMARK OFFICE **CERTIFICATE OF CORRECTION**

PATENT NO. : 7,158,598 B2

Page 1 of 1

APPLICATION NO.: 09/981903
DATED: January 2, 2007
INVENTOR(S): Markus Schetelig et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 10, Line 13:

Please delete "signal calculation" and insert --signal, calculation--

Column 12, Line 18:

Please delete "voltage quotas and" and insert --voltage quota, and--

Signed and Sealed this

Nineteenth Day of June, 2007

JON W. DUDAS
Director of the United States Patent and Trademark Office